



AICTE Sponsored Faculty Development Program
on
Application of Advanced Signal Processing for Operation, Control and Protection of Modern Power System with Integration of Large Scale Sustainable Energy Sources

22-11-2017 to 5-12-2017,(Wednesday to Tuesday)

**Venue: Department of Electrical Engineering
Indira Gandhi Institute of Technology, Sarang,Dhenkanal, Odisha-759146**

Course Objective: This program proposes the application of Advanced Signal Processing Techniques for reliable Operation,Control and Protection of Modern Power System integrated with Sustainable Energy Sources(SES). Further, the issues, challenges and impacts of SES integrated with grid will be addressed and the concept of Distribution Generation (DG), Smart grid and its protection aspect will be discussed at large in this program.

Important Dates:

- The filled in registration form should reach the Coordinators on or before 10-11-2017(Friday)
- The Applicants will be informed about their participation by 14-11-2017(Monday) by email
- Please visit our website www.igitsarang.ac.in for downloading registration form and other details

Highlights:

- The faculties from reputed institutes like IISc, IITs, NITs , IGIT, Sarang and from other well known Organization will deliver lectures in this program
- The participants from AICTE approved Institution will be provided to and fro train/bus fare through shortest route. The reimbursement of the TA will be made as per AICTE norms
- Participants will be provided free lodging and boarding in the Guest House/Hostels of IGIT Sarang on twin sharing basis

For any query please write at : fdpelectrical2017@gmail.com
Thanks & Reragds

Coordinators

Dr. Lokanath Tripathy, M- 9438324244

Dr. Pranati Das, M- 9437567599

Registration Form
AICTE SPONSORED FACULTY
DEVELOPMENT PROGRAMME
(22.11.2017 to 5.12.2017)

1. Name (Block Letters):
2. Designation & Dept:
3. Organization:
4. Address for communication:
Pin code:
Ph. No.:
5. Highest Academic Qualification:
6. Years of Experience:
7. Specialization:
8. Accommodation: Required / Not Required
Details of Remittance: Amount in Rs 300/-

Draft No.:

Bank

(Rs.300/- In favor of
FDP Electrical-2017, payable at Sarang)

Date: _____ Signature of the applicant

Head of the institution/Sponsoring Authority
(Seal)

Important Dates :

- The last date for receipt of duly forwarded applications: 10th Nov. 2017.
- Confirmation of Selection: 11th-15th Nov. 2017
(Intimation by email only).

Patron: Prof. (Dr.) S .K Chand
Director, IGIT, Sarang

Organizing Committee

Chair Person: Prof (Dr.) B.P Panigrahi
Professor and Head, EE Dept.

Members:

1. Dr. R. Behera, Assistant Professor
2. Sri M P Behera, Assistant Professor
3. Sri. B.D Biswal, Assistant Professor
4. Sri U. K. Mahanta, Assistant Professor
5. Sri B. Kumar, Assistant Professor
6. Sri K C Pradhan, Assistant Professor
7. Sri B K Nayak, Assistant Professor



Address for Correspondence:

Dr. Lokanath Tripathy
Dr. Pranati Das
Department of Electrical Engineering
Indira Gandhi Institute of Technology
Sarang, Dhenkanal, Odisha-759146

Email: loknathtripathy@yahoo.co.in
daspranati@yahoo.co.in

Contact Nos:

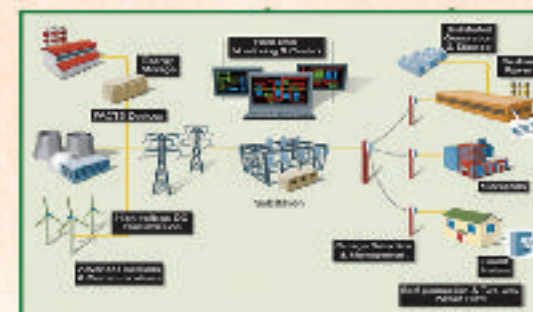
09438324244 (Dr. Lokanath Tripathy)
09437565799 (Dr. (Mrs) Pranati Das)

Official email id: fdpelectrical2017@gmail.com



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Coordinators

Dr. Lokanath Tripathy
(Associate Professor)
Dr.(Mrs) Pranati Das
(Associate Professor)

Organized by



Department of Electrical Engineering
Indira Gandhi Institute of Technology
Sarang, Dhenkanal, Odisha-759146
Website: www.igitsarang.ac.in

Host Institute

Indira Gandhi Institute of Technology is an autonomous technical institute under Govt. of Odisha Established in the year 1982. It has the distinction of being the 1st technical institute of Odisha to get accreditation from National Board of Accreditation of AICTE in 2004. The institute offers Diploma, MCA, B.Tech and M.Tech programs in various disciplines to meet the growing technical manpower need of the state and country. The institute also offers Ph.D programs in Civil Engg., Electrical Engg. and Mechanical Engg. under quality improvement programme (QIP) of AICTE, New Delhi since 2012-13 and also acts as the Nodal Centre for PhD program in Engineering/Technology under Utkal University, Odisha.

Department

The Department of Electrical Engineering of Indira Gandhi Institute of Technology, Sarang offers Diploma, B.Tech and four M.Tech Programs in Power System Engineering, Power Electronics and Drives, Energy System Engineering and Industrial Power Control & Drives (Part-Time) to cater to the ever challenging needs of technical excellence in the area of Electrical Engineering. The department has well equipped laboratories like Power System Lab, Electrical Machine Lab, Control System Lab, Simulation Lab, Basic Electrical Engineering Lab, Power Electronic and Drives Lab and Energy System Lab. Advanced computing facilities and softwares like MATLAB, Mi-Power, LABVIEW, PSCAD and PSim are also available in Computational Laboratory of the department for doing research in the area of cutting edge technologies. The infrastructure and lab facilities of the department are being upgraded from time to time and provide adequate opportunity for students

and researchers to learn and innovate. The department has distinguished faculties holding Master/ PhD degrees from renowned institution in India. The faculties of the department have been constantly carrying out research in many advanced area of electrical engineering and regularly publish research papers in international and national journals. The department also undertaken many research projects sponsored by the Government of India.

Course Objective

Objective of the program is to acquaint the participants with the application of Advanced Signal Processing Techniques for reliable Operation, Control and Protection of Modern Power System integrated with Sustainable Energy Sources (SES) Further, the issues, challenges and impacts of SES integrated with grid will be addressed and the concept of Distribution Generation (DG), Smart Grid and its protection aspect will be discussed at large in this program. This FDP will definitely familiarize the participants with the recent developments and innovations in the area of Modern Power System, SES, DG, Smart Grid and different signal processing techniques.

Course Contents

- Signal Processing Techniques applied to Power Signals
- Flexible AC Transmission (FACT) Devices and its application in Power System
- Modern Power System, issues and challenges
- Integration of SES and Micro-Grid
- Control/Operation Challenges of Modern Power System.
- Protection issues of Transmission System integrated with SES and FACT devices
- Distribution Generation(DG) and Smart Grid
- Tutorial at Laboratory

Resource Persons

The faculties from reputed institutes like IISc, IITs, NITs, IGIT, Sarang and from other well known Organization will deliver lectures in this program.

Facilities

Participants will be provided free lodging and boarding in the Guest House / Hostels of IGIT, Sarang. The participants from AICTE approved Institution will be provided to and fro train/bus fare through shortest route. The reimbursement of the TA will be made as per AICTE norms.

Eligibility and Registration

Faculty members from AICTE approved Institutions are eligible to participate in the program. Registration fees of Rs 300/- in the shape of DD drawn in favor of FDP, Electrical-2017, payable at Sarang (IFSC Code SBIN0010246) is to be sent along with filled in registration form, forwarded by the concerned authority to the Coordinator(s) on or before 10-11-2017 towards confirmation for participation in the program. The above registration fee will be refunded to the participants after joining and completing the program.

Venue

Indira Gandhi Institute of Technology, Sarang is situated on the bank of river Brahmani, 8 kms from Talcher. The institute is located at a distance of 160 kms from Bhubaneswar and is well connected by rail and road. The nearest bus stop is Balhar Chhak and nearest Railway Station is Talcher road. Taxis and auto rickshaws are available at those points. The climate at Sarang is pleasant during November-December.



Registration Form
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Details of Remittance: Amount of Rs 300/-

Draft No.:..... Bank.....

(Rs300/- In favour of FDP Electrical-2017 payable at Sarang)

Date:

Signature of the applicant

**Signature of the Head of the institution/Sponsoring Authority
(With Date and seal)**